



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Satyendra Yadav et al. Art Unit: 2665
Serial No.: 09/608,307 Examiner: M. Phan
Assignee : Intel Corporation
Filed : June 30, 2000
Title : PACKET PROCESSING IN A ROUTER ARCHITECTURE

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

In response to the Office action mailed April 28, 2004,
please consider the following.

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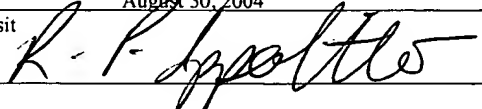
Amendments to the Claims begin on page 2 of this paper. SEP 07 2004

Remarks begin on page 12 of this paper.

Technology Center 2600

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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